	Express Mail	No. EV3355	2293805			Attorney Docket No. 108298633US Disclosure No. 01-0389		
6	6				COMPLETE IF KNOWN			
10	, 4	Ì			Application Number	10/034,924		
1	0 . %		DISCLOSURE		Confirmation Number	1417		
\		STATEMENT E			Filing Date	December 26, 2001		
N	WINT & THANKS	Form PTO-14	49 (Modified)		First Named Inventor	Stephen Moxham		
- 1	(1)	Use several she	ets if necessary	1)	Group Art Unit	2826		
- {					Examiner Name	Alexander O. Williams		
[Sheet	1	of	2	Attorney Docket No.	108298633US		

				U	.S. PATENT DOCUMENTS			_
Examiner Initials*	Cite No.	U.S. Patent or Application Kind Code NUMBER (if known)			Name of Patentee or Inventor of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Whi Relevant Passages or Relevant Figures Appear	
nu		5,255,156		T	Chang	10/19/1993		
. 1		5,286	5,679		Farnworth et al.	02/15/1994		
		5,44	4,303		Greenwood et al.	08/22/1995		_
		5,504,373			Oh et al.	04/02/1996		_
		5,54	4,124		Zagar et al.	08/06/1996		
		5,642	2,262		Terrill et al.	06/24/1997		_
		5,656	3,863		Yasunaga et al.	08/12/1997		
.	5,723,906		Rush	03/03/1998				
71		5,734,559			Banerjee et al.	03/31/1998		
		5,843,799			Hsu et al.	12/01/1998		
		5,936,844			Walton	08/10/1999		
\Box		5,946,314			Licciardi et al.	08/31/1999		
TI		6,048,755 6,108,210 6,111,205 6,118,179 6,159,764 6,180,504 B			Jiang et al.	04/11/2000		
					Chung	08/22/2000		
					Leddige et al.	08/29/2000		
					Farnworth et al.	09/12/2000		
					Wood et al.	11/21/2000		
\neg					Kinsman et al.	12/12/2000		_
-//				B1	Farnworth et al.	01/30/2001		_
-1		6,232,147 B1		B1	Matsuki et al.	05/15/2001		
on		6,429,528 B1		B1	King et al.	08/06/2002		
				FOR	EIGN PATENT DOCUMENTS	·		
xaminer 'nitrals*	Cite No	Foreign Patent or Application Kind Code Office NUMBER (if known)			Name of Patentee or Applicam of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Ι,
pn		EP	0 398 628		Kabushiki Kaisha Toshiba	11/22/1990		Γ

*EXAMINER Initial if reference considered, whether or not criteria is in conformance with MPEP 509. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to application(s).

CLAMINER Initial if reference considered, whether or not criteria is in conformance with MPEP 509. Draw line through citation if not in conformance and not considered. Whether the communication is application(s). EXAMINER DATE CONSIDERED



Sheet

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Form PTO-1449 (Modified) (Use several sheets if necessary)

of

2

FOREIGN DATENT DOCUMENTS

2

COMPLETE IF KNOWN 10/034,924 Application Number Confirmation Number 1417 Filing Date December 26, 2001 First Named Inventor Stephen Moxham Group Art Unit 2826 Examiner Name Alexander O Williams Attorney Docket No. 108298633US

				FOR	EIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No.	Foreign Patent or Application Kind Code Office NUMBER (If known)			Name of Patentee or Applicant of Cited Occurrent	Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	,		
.pn	140	EP	0 413 451	A2 A3	Inmos Ltd.	02/20/1991				
os		wo	92/17901	A1	Integrated System Assemblies Corporation	10/15/1992				
p	#	DE	196 00 401	. A1	Siemens AG	07/17/1997				
gr-	#	DE	197 14 470	- A1	Hewlett-Packard Co.	12/11/1997				
n		EP	0 853 337	. A1	Fujitsu Ltd.	07/15/1998				
			OTHER PR	OR AR	T-NON PATENT LITERATURE	DOCUMENTS		1		
include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item Examiner C4e (book, magazine, journal, serial, symposum, catalog, etc.), date, page(s), volume issue number(s), publisher, city Indials* No Another C4e (book, magazine, journal, serial, symposum, catalog, etc.), date, page(s), volume issue number(s), publisher, city and/or country where published.								т		
a	_	VAN ZANT, Peter, "Microchip Fabrication: A Practical Guide to Semiconductor Processing," page 545, McGraw-Hill, New York, Third Edition, October 1996, ISBN: 0070672504.								
		HARPER, Charles A. and MILLER, Martin B., "Electronic Packaging, Microelectronics, and Interconnection Dictionary," page 211, McGraw-Hill, New York, February 1993, ISBN: 0070266883.								
		KOHL, James E. et al., "Low Cost Chip Scale Packaging and Interconnect Technology," 7 pages, Proceedings of the Surface Mount International Conference, San Jose, California, September 1997, http://www.epic-tech.com/smtppr.pdf >.								
		KOHL, James E. et al., "EPIC CSP™ Assembly and Reliability Methods," 5 pages, Proceedings of CSI98, Santa Clara, California, May 1998, http://www.epic-tech.com/cspi982.pdf .								
		SINGER, Adam T. et al., "Cost Considerations for CSP Variations," 9 pages, presented at Chip Scale International, San Jose, California, May 1998. http://www.epic-tech.com/ibis_papr.pdf .								
an	TESSIER, Theodore G. et al., "Technologies and Trends in Wafer Level Packaging," 15 pages, Future Fab International, Volume 11, June 29, 2001, http://www.future-fab.com/documents.asp?d_ID=660 .									
EXAMINE	d	10,	/MM		DATE CONSIDERED	11/17/03				
'EXAMINE					criteria is in conformance with MPEP 609 h next communication to application(s).	Draw line through c	tation if not in conformance	and		
Q \Clients\Mic			(18633)US\IDSsupp Form 14							